

501.40692X00

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Ko Miyazaki et al

Serial No.: 09/964,490

Filed: September 28, 2001

For: FABRICATION METHOD OF SEMICONDUCTOR  
INTEGRATED CIRCUIT DEVICE

Art Unit: 2125

Examiner: Shechtman, S.

**AMENDMENT**

Mail Stop: Amendment  
Commissioner For Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 21, 2005

Sir:

This is in response to the Office Action mailed October 21, 2004, in connection with the above-identified application. Amendments to the application are listed below and are set forth on the following pages.

Amendments to the Specification;

Amendments to the Claims; and

Remarks are included following the amendments.